

Addendum to “Service Automata”

– formal proofs –

Richard Gay, Heiko Mantel, Barbara Sprick

Department of Computer Science, TU Darmstadt, Germany
{gay,mantel,sprick}@mais.informatik.tu-darmstadt.de

November 16, 2011

This document contains the formal proof for Theorem 1 in the article “Service Automata” [1] (Section 2) and preliminaries for the formalism (Section 1).

1 Preliminaries

In the proof, we denote the length of a sequence s by $|s|$, and the projection of a sequence s to an alphabet E of events by $s \upharpoonright E$. As a shorthand, we write $e_1, e_2 \triangleleft t$ for $e_1 \triangleleft t$ and $e_2 \triangleleft t$.

1.1 A Primer to Hoare’s Communicating Sequential Processes

We briefly recall the sublanguage of Hoare’s Communicating Sequential Processes (CSP) used in this article. For a proper introduction, we refer to [2].

A *process* P is a pair $(\alpha(P), \text{traces}(P))$ consisting of a set of events and a nonempty, prefix-closed set of finite sequences over $\alpha(P)$. The *alphabet* $\alpha(P)$ contains all events in which P could in principle engage. The *set of possible traces* $\text{traces}(P) \subseteq (\alpha(P))^*$ contains all sequences of events that the process could in principle perform. We use $\langle \rangle$ to denote the empty sequence, $\langle e \rangle$ to denote the trace consisting of the single event e , and $s.t$ to denote the concatenation of two traces s and t . That an event e occurs in a trace t is denoted by $e \triangleleft t$.

The CSP process expression STOP_E specifies a process with alphabet E and a set of traces containing only $\langle \rangle$. A process that performs event e and then behaves according to the process expression P , is specified by $e \rightarrow P$. External and internal choice between P and Q are specified by $P \square Q$ and $P \sqcap Q$, respectively. They model that the process behaves according to either P or Q . The parallel composition of P and Q is specified by $P \parallel Q$. The parallel processes have to synchronize on the occurrences of all events that their alphabets have in common. The process $P \setminus E$ behaves as P but all events in the set E are hidden by removing them from the process’ alphabet and possible traces.

The binary operators \square , \sqcap and \parallel are lifted to n -ary operators over non-empty finite index sets. For instance, $\square_{x \in X} P(x)$ equals $P(a)$ if $X = \{a\}$ and equals $P(a) \square (\square_{x \in X \setminus \{a\}} P(x))$ if $a \in X$ and X contains at least two elements.

We use structured events of the form $c.m$ to model the communication of a message m on a channel c . In a process expression we write $c!m$ instead of $c.m$ in order to indicate that message m is sent on c , and use $c?x: M$ for receiving some message $m \in M$ on channel c while instantiating

the variable x with m . Effectively, $c?x: M \rightarrow P(x)$ corresponds to an external choice on the events in $\{c.m \mid m \in M\}$ such that the computation continues according to $P(m)$.

A process definition $\text{NAME} \stackrel{\text{def}}{=}_{\alpha} P$ declares a new process name NAME and defines that NAME models a process whose traces are given by the process expression P and whose alphabet equals α . We omit the subscript α in a process definition if the alphabet of NAME shall equal $\alpha(P)$. Process names can be used as subexpressions within process expressions, thus allowing for recursion.

Properties of CSP processes are modeled by unary predicates on traces. We say that a unary predicate φ on traces is *satisfied* by a process P (denoted by $P \text{ sat } \varphi$) if and only if $\varphi(t)$ holds for each $t \in \text{traces}(P)$.

In the proofs, we denote by P/t the process P after engaging in the sequence of events t .

1.2 Semantics of CSP

The following lists the trace semantics for the CSP constructs used in the paper.

- for every set E of events, $\alpha(\text{STOP}_E) = E$ and $\text{traces}(\text{STOP}_E) = \{\langle \rangle\}$;
- for process P and event $e \in \alpha(P)$, $\alpha(e \rightarrow P) = \alpha(P)$ and $\text{traces}(e \rightarrow P) = \{\langle \rangle\} \cup \{\langle e \rangle.tr \mid tr \in \text{traces}(P)\}$;
- for process P , channel c , and message m , if $c.m \in \alpha(P)$, then $\alpha(c!m \rightarrow P) = \alpha(P)$ and $\text{traces}(c!m \rightarrow P) = \{\langle \rangle\} \cup \{\langle c.m \rangle.tr \mid tr \in \text{traces}(P)\}$;
- for channel c , set M , and process expression $P(x)$ parametric in $x \in M$, if $\alpha(P(m)) = \alpha(P(m'))$ and $c.m \in \alpha(P(m))$ holds for all $m, m' \in M$, then $\alpha(c?x: M \rightarrow P(x)) = \alpha(P(m))$ for any $m \in M$ and $\text{traces}(c?x: M \rightarrow P(x)) = \{\langle \rangle\} \cup \{\langle c.x \rangle.tr \mid x \in M \wedge tr \in \text{traces}(P(x))\}$;
- for processes P and Q with $\alpha(P) = \alpha(Q)$, $\alpha(P \square Q) = \alpha(P) = \alpha(Q)$ and $\text{traces}(P \square Q) = \text{traces}(P) \cup \text{traces}(Q)$;
- for a non-empty finite set X and a process expression $P(x)$ parametric in x , if $\alpha(P(x)) = \alpha(P(y))$ for all $x, y \in X$, then $\alpha(\square_{x \in X} P(x)) = \alpha(P(y))$ for any $y \in X$ and $\text{traces}(\square_{x \in X} P(x)) = \bigcup_{x \in X} \text{traces}(P(x))$;
- for processes P and Q , $\alpha(P \parallel Q) = \alpha(P) \cup \alpha(Q)$ and $\text{traces}(P \parallel Q) = \{t \in \alpha(P \parallel Q)^* \mid t \upharpoonright \alpha(P) \in \text{traces}(P) \wedge t \upharpoonright \alpha(Q) \in \text{traces}(Q)\}$;
- for process P and set E of events, $\alpha(P \setminus E) = \alpha(P) \setminus E$ and $\text{traces}(P \setminus E) = \{t \upharpoonright \alpha(P \setminus E) \mid t \in \text{traces}(P)\}$.

In the trace semantics, we define two processes P and Q to be equal (written $P = Q$), if and only if $\alpha(P) = \alpha(Q)$ and $\text{traces}(P) = \text{traces}(Q)$ holds. The following recapitulates some basic properties of CSP processes provided by Hoare in [2].

Lemma 1. *Let P, Q be CSP processes, E, E' be sets of events, tr be a sequence of events, $e \in \alpha(P)$ be an event, and f be an injective alphabet renaming function. Then*

- (a) $f(P \parallel Q) = f(P) \parallel f(Q)$ [2, page 65 (L3)]
- (b) $P \setminus (E \cup E') = (P \setminus E) \setminus E'$ [2, page 92 (L2)]
- (c) $(P \parallel Q) \setminus E = (P \setminus E) \parallel (Q \setminus E)$ if $\alpha(P) \cap \alpha(Q) \cap E = \emptyset$ [2, page 92 (L6)]
- (d) $f(P \setminus E) = f(P) \setminus f(E)$ [2, page 92 (L7)]
- (e) $P / (s_1.s_2) = (P / s_1) / s_2$ [2, page 32 (L2)]
- (f) $(e \rightarrow P) / \langle e \rangle.tr = P / tr$ [2, page 32 (L2+L3)]
- (g) $(P \square Q) / tr = P / tr$ if $tr \in \text{traces}(P) \setminus \text{traces}(Q)$ and $(P \square Q) / tr = Q / tr$ if $tr \in \text{traces}(Q) \setminus \text{traces}(P)$ [2, page 88 (L2)]

2 Proof of Theorem 1

In this section, we give a formal proof for [1, Theorem 1], which shows that the service automata framework SYSTEM soundly enforces the Chinese Wall security policy.

Theorem 1. SYSTEM sat ChW .

The proof for the theorem can be found at the end of this section on page 9. To increase the readability of the proof, we show major steps of the proof in separate lemmas:

- Lemma 2 states that the hiding in the definitions of the service automata and the local policies can be moved outside of the controlled system, if local channels are renamed to be unique. Definition 1 specifies the renaming and the controlled system without hiding. This change of the hiding simplifies the reasoning about the events that are hidden in SYSTEM.
- Lemma 3 states that certain events cannot occur in traces of the instantiated service automata: (a) decisions are not sent on channel $ddec$, (b) requests are not sent on channel $rdec$, (c) requests and decisions are not forwarded, (d) remote decisions are not approved for locally decidable events, and (e) local enforcement decisions are not sent to an enforcer for events that have a remote responsible node.
- Lemma 4 states that an enforcer performs a critical event only after this enforcer has received a corresponding decision.
- Lemma 5 states that every performed critical event must have ultimately been permitted by its responsible node.
- Lemma 6 states that no decision-making component permits two conflicting events in a single trace.

In the remainder of this section, we abbreviate $COR_i(CE_i, ED_i)$ by COR_i , $INT_i(\alpha(\text{PROV}_i), CE_i)$ by INT_i , and $REPLACE(CE_i)$ by $REPLACE_i$. By DUM , we denote the set $\{dummy_{ev} \mid ev \in CE\}$ of all dummy events.

Definition 1. (a) Let $i \in Id$ be an identifier. We define the set of events hidden by the service automaton at node i as

$$H_i := \left\{ \begin{array}{l} \text{sync.}\checkmark, \text{icpt.}ev, \text{enf.}ed, \text{lreq.}ev, \text{rreq.}dr, \text{fwd.}(k, dr), \\ \text{edec.}ed, \text{appv.}ed, \text{ddec.}(k, dr), \text{rdec.}(k, dr) \end{array} \mid \begin{array}{l} ev \in CE_i, ed \in ED, \\ dr \in DR, k \in Id \setminus \{i\} \end{array} \right\}.$$

(b) For all identifiers $i \in Id$, let $\rho_i : E_i \rightarrow E_i \cup \{c_i.m \mid c.m \in H_i \cup H_i^{\text{pol}}\}$ be a renaming function on $E_i := \alpha((\text{PROV}_i \parallel \text{INT}_i) \setminus CE_i) \cup \alpha(\text{REPLACE}_i) \cup \alpha(\text{COR}_i) \cup \alpha(\text{DEL}_i) \cup \alpha(\text{DEC}_i(\emptyset)) \cup \alpha(\text{SRP}_i)$ such that

$$\rho_i(e) = \begin{cases} c_i.m & \text{if } e \in H_i \cup H_i^{\text{pol}} \text{ with } e = c.m, \\ e & \text{otherwise.} \end{cases}$$

Note that H_i^{pol} is defined in [1, page 12]. We lift the renaming functions from events to processes as in [2, Section 2.6], by applying the renaming to all events occurring in the respective process.

- (c) The set of all renamed hidden events is defined as $HS := \bigcup_{i \in Id} \rho_i(H_i \cup H_i^{\text{pol}})$,
(d) The system with internal events, SI , is defined as

$$SI \stackrel{\text{def}}{=} \parallel_{i \in Id} \left(\begin{array}{l} \rho_i((\text{PROV}_i \parallel \text{INT}_i) \setminus CE_i) \parallel \rho_i(\text{REPLACE}_i) \\ \parallel \rho_i(\text{COR}_i) \parallel \rho_i(\text{DEL}_i) \parallel \rho_i(\text{DEC}_i(\emptyset)) \parallel \rho_i(\text{SRP}_i) \end{array} \right).$$

Lemma 2. If $\alpha(\text{PROV}_i) \cap (HS \cup H_i^{\text{pol}}) = \emptyset$ holds for all identifiers $i \in Id$, then $SI \setminus HS = \text{SYSTEM}$.

Proof. For all identifiers $j \in Id$, the definitions of all processes $P \in \{\text{COR}_i, \text{DEL}_i, \text{DEC}_i, \text{SRP}_i \mid i \in Id \setminus \{j\}\}$ ensure $\alpha(P) \cap \rho_j(H_j \cup H_j^{\text{pol}}) = \emptyset$. The precondition $\alpha(\text{PROV}_i) \cap (HS \cup H_i^{\text{pol}}) = \emptyset$ together with $CE_i \subseteq \alpha(\text{PROV}_i)$ [1, page 11] and the definitions of the processes $REPLACE_i$ and INT_i implies $\alpha(Q) \cap \rho_j(H_j \cup H_j^{\text{pol}}) = \emptyset$ for all $Q \in \{\text{REPLACE}_i, (\text{PROV}_i \parallel \text{INT}_i) \setminus CE_i \mid i \in Id \setminus \{j\}\}$.

We denote the above two observations as (\dagger) below.

$$\begin{aligned}
\text{SI} \setminus \text{HS} &= \text{by Definition 1 (d) and Lemma 1 (a)} \\
&\left(\prod_{i \in Id} \rho_i \left(\left((\text{PROV}_i \parallel \text{INT}_i) \setminus \text{CE}_i \parallel \text{REPLACE}_i \right) \parallel \text{COR}_i \parallel \text{DEL}_i \parallel \text{DEC}_i(\emptyset) \parallel \text{SRP}_i \right) \right) \setminus \text{HS} \\
&= \text{by Definition 1 (c), Lemma 1 (c), and observations } (\dagger) \\
&\prod_{i \in Id} \left(\rho_i \left(\left((\text{PROV}_i \parallel \text{INT}_i) \setminus \text{CE}_i \parallel \text{REPLACE}_i \right) \parallel \text{COR}_i \parallel (\text{DEL}_i \parallel \text{DEC}_i(\emptyset) \parallel \text{SRP}_i) \right) \setminus \rho_i(H_i \cup H_i^{\text{pol}}) \right) \\
&= \text{by Lemma 1 (d)} \\
&\prod_{i \in Id} \rho_i \left(\left(\left((\text{PROV}_i \parallel \text{INT}_i) \setminus \text{CE}_i \parallel \text{REPLACE}_i \right) \parallel \text{COR}_i \parallel (\text{DEL}_i \parallel \text{DEC}_i(\emptyset) \parallel \text{SRP}_i) \right) \setminus (H_i \cup H_i^{\text{pol}}) \right) \\
&= \text{by Definition 1 (b), which gives } \rho_i(e) = e \text{ for all } e \notin H_i \cup H_i^{\text{pol}} \\
&\prod_{i \in Id} \left(\left((\text{PROV}_i \parallel \text{INT}_i) \setminus \text{CE}_i \parallel \text{REPLACE}_i \right) \parallel \text{COR}_i \parallel (\text{DEL}_i \parallel \text{DEC}_i(\emptyset) \parallel \text{SRP}_i) \right) \setminus (H_i \cup H_i^{\text{pol}}) \\
&= \text{by Lemma 1 (b); by precondition } \alpha(\text{PROV}_i) \cap H_i^{\text{pol}} = \emptyset; \\
&\quad \text{by the fact that for all } i \in Id \text{ and } P \in \{\text{INT}_i, \text{REPLACE}_i, \text{COR}_i\}, \alpha(P) \cap H_i^{\text{pol}} = \emptyset \\
&\quad \text{holds according to the definition of } P; \text{ and by definition of } \text{POL}_i \text{ [1, page 12]} \\
&\prod_{i \in Id} \left(\left((\text{PROV}_i \parallel \text{INT}_i) \setminus \text{CE}_i \right) \parallel \text{POL}_i \parallel \text{COR}_i \parallel \text{REPLACE}_i \right) \setminus H_i \\
&= \text{by definition of } \text{SA}_i \text{ [1, page 6] and } \text{SYSTEM} \text{ [1, page 13]} \\
&\prod_{i \in Id} \text{SA}_i(\text{PROV}_i, \text{CE}_i, \text{POL}_i, \text{ED}_i, \text{REPLACE}_i) = \text{SYSTEM} \quad \square
\end{aligned}$$

Remark 1. In the following, we repeatedly make use of the following patterns of reasoning.

- Let $tr \in \text{traces}(\text{SI})$ be a trace, P, Q be processes such that $\text{SI} = P \parallel Q$ holds, and $e \in \alpha(Q)$ be an event. Then from $e \triangleleft tr$ we can follow that $e \triangleleft tr \upharpoonright \alpha(Q) \wedge tr \upharpoonright \alpha(Q) \in \text{traces}(Q)$. We indicate this reasoning by $\xrightarrow{(*)}$.
- Let Q be a process, $e \in \alpha(Q)$ be an event, tr_Q be a trace, and $E \subseteq \alpha(Q)$ be the set of immediate predecessors of e in process Q . Then from $e \triangleleft tr_Q \wedge tr_Q \in \text{traces}(Q)$ we can conclude that $\bigvee_{e' \in E} (e' \triangleleft tr_Q)$ holds. We use $\xrightarrow{(**)}$ to indicate this reasoning.
- We often combine the above steps in the form $e \triangleleft tr \xrightarrow{(*)} e \triangleleft tr_Q \wedge tr_Q \in \text{traces}(Q) \xrightarrow{(**)} \bigvee_{e' \in E} (e' \triangleleft tr_Q) \implies \bigvee_{e' \in E} (e' \triangleleft tr)$ for $tr_Q = tr \upharpoonright \alpha(Q)$. The last implication trivially holds. In the remainder of this section, we abbreviate such chains of reasoning by $e \triangleleft tr \xrightarrow{Q} \bigvee_{e' \in E} (e' \triangleleft tr)$.

Lemma 3. Let $\text{CE}_i^{\text{ld}} := \{ev \in \text{CE}_i \mid \text{id}(ev) = \text{resp}(ev)\}$ be the set of locally decidable critical events at node $i \in Id$. Then for all traces $tr \in \text{SI}$ and identifiers $i, j, k \in Id$ with $j \neq i$, the following holds:

- For all decisions $ed \in \text{ED}$, it holds that $\text{ddec}_i.(j, (k, ed)) \not\triangleleft tr$.
- For all events $ev \in \text{CE}$, it holds that $\text{rdec}_i.(j, (k, ev)) \not\triangleleft tr$.
- For all events and decisions $x \in \text{CE} \cup \text{ED}$, it holds that $\text{fwd}_j.(i, (i, x)) \not\triangleleft tr$.
- For all decisions $ed \in \text{CE}_i^{\text{ld}} \times \text{CE}_i$, it holds that $\text{appv}_i.ed \not\triangleleft tr$.
- For all decisions $ed \in \text{ED}_i \setminus (\text{CE}_i^{\text{ld}} \times \text{CE}_i)$ it holds that $\text{edec}_i.ed \not\triangleleft tr$.

Proof. Let the trace $tr \in \text{traces}(\text{SI})$ be arbitrary but fixed.

- (a) Let identifiers $i, j, k \in Id$ with $i \neq j$ and decision $ed \in ED$ be arbitrary but fixed. We show the claim by contradiction and assume $\text{ddec}_i.(j, (k, ed)) \triangleleft tr$.

$$\begin{aligned}
& \xrightarrow{(*)} \text{ddec}_i.(j, (k, ed)) \triangleleft tr \uparrow \alpha(\rho_i(\text{SRP}_i)) \wedge tr \uparrow \alpha(\rho_i(\text{SRP}_i)) \in \text{traces}(\rho_i(\text{SRP}_i)) \\
& \implies \text{by definition of } \text{SRP}_i \\
& \quad (k, ed) \in Id \times CE \\
& \implies \text{by the precondition } Id \times CE \cap Id \times ED = \emptyset \text{ for SRP [1, page 9]} \\
& \quad (k, ed) \notin Id \times ED
\end{aligned}$$

This contradicts the preconditions $k \in Id$ and $ed \in ED$. Hence, the assumption of $\text{ddec}_i.(j, (k, ed)) \triangleleft tr$ is wrong and $\text{ddec}_i.(j, (k, ed)) \not\triangleleft tr$ holds.

- (b) The proof goes along the lines of the one for the previous part, with ED exchanged by CE and ddec exchanged by rdec .
- (c) We show the claim by contradiction and assume that there exist $x \in CE \cup ED$ and $i, j \in Id$ with $j \neq i$, such that $\text{fwd}_j.(i, (i, x)) \triangleleft tr$ holds.

$$\begin{aligned}
& \xrightarrow{\rho_j(\text{SRP}_j)} \text{rreq}_j.(i, x) \triangleleft tr \\
& \xrightarrow{\rho_j(\text{COR}_j)} \text{link}_{k,j}.(i, x) \triangleleft tr \text{ for some identifier } k \neq j \\
& \xrightarrow{\rho_k(\text{COR}_k)} \text{ddec}_k.(j, (i, x)) \triangleleft tr \vee \text{rdec}_k.(j, (i, x)) \triangleleft tr \vee \text{fwd}_k.(j, (i, x)) \triangleleft tr \\
& \xrightarrow{(*)} \text{ddec}_k.(j, (i, x)) \triangleleft tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \wedge tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \in \text{traces}(\rho_k(\text{SRP}_k)) \\
& \quad \vee \text{rdec}_k.(j, (i, x)) \triangleleft tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \wedge tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \in \text{traces}(\rho_k(\text{SRP}_k)) \\
& \quad \vee \text{fwd}_k.(j, (i, x)) \triangleleft tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \wedge tr \uparrow \alpha(\rho_k(\text{SRP}_k)) \in \text{traces}(\rho_k(\text{SRP}_k)) \\
& \implies \text{by definition of } \text{SRP}_k \\
& \quad j = \text{next}(k, i) \\
& \implies \text{by definition of } \text{next} \text{ [1, page 12]} \\
& \quad j = \text{next}(k, i) = i
\end{aligned}$$

The last equation contradicts the initial assumption of $i \neq j$. Hence, the assumption $\text{fwd}_j.(i, (i, x)) \triangleleft tr$ cannot hold, i.e., $\text{fwd}_j.(i, (i, x)) \not\triangleleft tr$ holds for all $j \neq i$.

- (d) We show the claim by contradiction and assume there exists an identifier $i \in Id$ and a decision $ed = (ev, ev') \in CE_i^{\text{ld}} \times CE_i$ such that $\text{appv}_i.ed \triangleleft tr$ holds.

$$\begin{aligned}
& \xrightarrow{\rho_i(\text{SRP}_i)} \text{rreq}_i.(i, ed) \triangleleft tr \xrightarrow{\rho_i(\text{COR}_i)} \text{link}_{j,i}.(i, ed) \triangleleft tr \text{ for some identifier } j \neq i \\
& \xrightarrow{\rho_j(\text{COR}_j)} \text{ddec}_j.(i, (i, ed)) \triangleleft tr \vee \text{fwd}_j.(i, (i, ed)) \triangleleft tr \vee \text{rdec}_j.(i, (i, ed)) \triangleleft tr \\
& \implies \text{by Lemma 3 (a) and Lemma 3 (c)} \\
& \quad \text{rdec}_j.(i, (i, ed)) \triangleleft tr \\
& \xrightarrow{\rho_j(\text{SRP}_j)} \text{rtrsp}_j.(i, ed) \triangleleft tr \xrightarrow{\rho_j(\text{DEC}_j(\emptyset))} \text{rreq}_j.ev \triangleleft tr \xrightarrow{\rho_j(\text{SRP}_j)} \text{rreq}_j.(j, ev) \triangleleft tr \\
& \xrightarrow{\rho_j(\text{COR}_j)} \text{link}_{k,j}.(j, ev) \triangleleft tr \text{ for some identifier } k \neq j \\
& \xrightarrow{\rho_k(\text{COR}_k)} \text{ddec}_k.(j, (j, ev)) \triangleleft tr \vee \text{fwd}_k.(j, (j, ev)) \triangleleft tr \vee \text{rdec}_k.(j, (j, ev)) \triangleleft tr
\end{aligned}$$

\implies by Lemma 3 (b) and Lemma 3 (c)
 $\text{ddec}_k.(j, (j, ev)) \triangleleft tr$
 $\xrightarrow{\rho_k(\text{SRP}_k)}$ $\text{rtreq}_k.(j, ev) \triangleleft tr$
 $\xrightarrow{(*)}$ $\text{rtreq}_k.(j, ev) \triangleleft tr \upharpoonright \alpha(\rho_k(\text{DEL}_k)) \wedge tr \upharpoonright \alpha(\rho_k(\text{DEL}_k)) \in \text{traces}(\rho_k(\text{DEL}_k))$
 \implies by definition of DEL_k , according to which $\text{rtreq}_k.(j, ev)$
can only occur if $ev \in \{ev' \in CE_k \mid k \neq \text{resp}(ev')\}$ holds
 $ev \in CE_k \wedge k \neq \text{resp}(ev)$
 \implies by definition of id [1, page 11]
 $id(ev) \neq \text{resp}(ev)$
 \implies by definition of CE_i^{ld}
 $ev \notin CE_i^{\text{ld}}$

The final consequence contradicts the initial assumption of $ed = (ev, ev') \in CE_i^{\text{ld}} \times CE_i$. Hence, the assumption $\text{appv}_i.ed \triangleleft tr$ is wrong and $\text{appv}_i.ed \not\triangleleft tr$ holds.

- (e) Let identifier $i \in Id$ and decision $ed = (ev, ev') \in ED_i \setminus (CE_i^{\text{ld}} \times CE_i)$ be arbitrary but fixed. We show the claim by contradiction and assume $\text{edec}_i.ed \triangleleft tr$ holds.

$\xrightarrow{\rho_i(\text{DEC}_i(\emptyset))}$ $\text{lereq}_i.ev \triangleleft tr$
 $\xrightarrow{(*)}$ $\text{lereq}_i.ev \triangleleft tr \upharpoonright \alpha(\rho_i(\text{DEL}_i)) \wedge tr \upharpoonright \alpha(\rho_i(\text{DEL}_i)) \in \text{traces}(\rho_i(\text{DEL}_i))$
 \implies by definition of DEL_i , according to which $\text{lereq}_i.ev$
can only occur if $ev \in \{ev' \in CE_i \mid i = \text{resp}(ev')\}$
 $i = \text{resp}(ev)$
 \implies by the precondition that $ev \in CE_i$ and by definition of CE_i^{ld}
 $ev \in CE_i^{\text{ld}}$

The final statement contradicts the precondition $ed = (ev, ev') \in ED_i \setminus (CE_i^{\text{ld}} \times CE_i)$, from which $ev \notin CE_i^{\text{ld}}$ follows immediately. \square

Lemma 4. For all events $ev \in CE$ and traces $tr \in \text{traces}(\text{Sl})$ with $ev \triangleleft tr$, it holds that there exists an event $ev' \in CE_{id(ev)}$ with $\text{enf}_{id(ev)}.(ev', ev) \triangleleft tr$.

Proof. Let $ev \in CE$ be an event and $tr \in \text{traces}(\text{Sl})$ be a trace with $ev \triangleleft tr$. Let $i := id(ev)$.

$\xrightarrow{(*)}$ with $ev \in CE_{id(ev)} = CE_i$ by definition of id [1, page 11],
 $CE_i \subseteq \alpha(\text{REPLACE}_i)$ by definition of $\text{REPLACE}_i = \text{REPLACE}(CE_i)$ [1, page 8],
and $\rho_i(ev) = ev$ according to Definition 1 (b)
 $ev \triangleleft tr \upharpoonright \alpha(\rho_i(\text{REPLACE}_i)) \wedge tr \upharpoonright \alpha(\rho_i(\text{REPLACE}_i)) \in \text{traces}(\rho_i(\text{REPLACE}_i))$
 $\xrightarrow{(**)}$ by the precondition that CE_i , defined in [1, page 11],
is disjoint from the set $\{\text{sync}.\checkmark, \text{enf}.(ev', ev) \mid ev, ev' \in CE_i\}$
 $\text{enf}_i.(ev', ev) \triangleleft tr \upharpoonright \alpha(\rho_i(\text{REPLACE}_i))$ for some event $ev' \in CE_i$
 \implies with $i = id(ev)$
 $\text{enf}_{id(ev)}.(ev', ev) \triangleleft tr$

Note that the second implication holds, because we concretized the replacement sequences EA as CE_i (i.e., sequences of length 1 of critical events) in [1, page 12]. Therefore, a simplified equivalent specification of the replacer process is

$$\text{REPLACE}_i = \text{enf}?(ev', ev): CE_i \times CE_i \rightarrow ev \rightarrow \text{sync!}\checkmark \rightarrow \text{REPLACE}_i \quad \square$$

Lemma 5. *Let $ev \in CE \setminus DUM$ be an event and $tr \in \text{traces}(\text{SI})$ with $ev \triangleleft tr$ be a trace. Then $\text{edec}_{\text{resp}(ev)}.(ev, ev) \triangleleft tr$ or $\text{rtrsp}_{\text{resp}(ev)}.(id(ev), (ev, ev)) \triangleleft tr$ holds true.*

Proof. Let $ev \in CE \setminus DUM$ be an event and $tr \in \text{traces}(\text{SI})$ be a trace with $ev \triangleleft tr$. Let $i := id(ev)$.

$$\begin{aligned} &\implies \text{by Lemma 4} \\ &\quad \text{enf}_i.(ev', ev) \triangleleft tr \text{ for some event } ev' \in CE_i \\ &\xrightarrow{\rho_i(\text{COR}_i)} \text{edec}_i.(ev', ev) \triangleleft tr \vee \text{appv}_i.(ev', ev) \triangleleft tr \\ &\implies \text{by Lemma 3 (d) and Lemma 3 (e)} \\ &\quad (\text{edec}_i.(ev', ev) \triangleleft tr \wedge id(ev') = \text{resp}(ev')) \vee (\text{appv}_i.(ev', ev) \triangleleft tr \wedge id(ev') \neq \text{resp}(ev')) \end{aligned}$$

In the following, we show the claim for both disjuncts separately.

$$\begin{aligned} \text{(a)} \quad &\text{edec}_i.(ev', ev) \triangleleft tr \wedge id(ev') = \text{resp}(ev') \\ &\xrightarrow{(*)} \text{edec}_i.(ev', ev) \triangleleft tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \wedge tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset))) \\ &\implies \text{by definition of } \text{DEC}_i(\emptyset) \\ &\quad \text{edec}_i.(ev', ev) \triangleleft tr \wedge (ev' = ev \vee ev = \text{dummy}_{ev'}) \\ &\implies \text{by the preconditions } ev \in CE \setminus DUM \text{ and } \text{dummy}_{ev'} \in DUM \\ &\quad \text{edec}_i.(ev', ev) \triangleleft tr \wedge ev = ev' \\ &\implies \text{by the preconditions } i = id(ev) \text{ and } id(ev') = \text{resp}(ev') \\ &\quad \text{edec}_{\text{resp}(ev)}.(ev, ev) \triangleleft tr \\ \\ \text{(b)} \quad &\text{appv}_i.(ev', ev) \triangleleft tr \wedge id(ev') \neq \text{resp}(ev') \\ &\xrightarrow{\rho_i(\text{SRP}_i)} \text{rreq}_i.(i, (ev', ev)) \triangleleft tr \\ &\xrightarrow{\rho_i(\text{COR}_i)} \text{link}_{j,i}.(i, (ev', ev)) \triangleleft tr \text{ for some identifier } j \neq i \\ &\xrightarrow{\rho_j(\text{COR}_j)} \text{ddec}_j.(i, (i, (ev', ev))) \triangleleft tr \vee \text{fwd}_j.(i, (i, (ev', ev))) \triangleleft tr \vee \text{rdec}_j.(i, (i, (ev', ev))) \triangleleft tr \\ &\implies \text{by Lemma 3 (a) and Lemma 3 (c)} \\ &\quad \text{rdec}_j.(i, (i, (ev', ev))) \triangleleft tr \\ &\xrightarrow{\rho_j(\text{SRP}_j)} \text{rtrsp}_j.(i, (ev', ev)) \triangleleft tr \tag{\dagger} \\ &\xrightarrow{(*)} \text{rtrsp}_j.(i, (ev', ev)) \triangleleft tr \upharpoonright \alpha(\rho_j(\text{DEC}_j(\emptyset))) \wedge tr \upharpoonright \alpha(\rho_j(\text{DEC}_j(\emptyset))) \in \text{traces}(\rho_j(\text{DEC}_j(\emptyset))) \\ &\xrightarrow{(**)} \text{by definition of } \text{DEC}_j(\emptyset) \\ &\quad \text{rreq}_j.ev' \triangleleft tr \upharpoonright \alpha(\rho_j(\text{DEC}_j(\emptyset))) \wedge (ev = ev' \vee ev = \text{dummy}_{ev'}) \\ &\implies \text{by the preconditions } ev \in CE \setminus DUM \text{ and } \text{dummy}_{ev'} \in DUM \end{aligned}$$

$$\begin{aligned}
& \text{rreq}_j.ev' \triangleleft tr \upharpoonright \alpha(\rho_j(\text{DEC}_j(\emptyset))) \wedge ev = ev' & (\ddagger) \\
\implies & \text{rreq}_j.ev \triangleleft tr \\
\stackrel{\rho_j(\text{SRP}_j)}{\implies} & \text{rreq}_j.(j, ev) \triangleleft tr \\
\stackrel{\rho_j(\text{COR}_j)}{\implies} & \text{link}_{k,j}.(j, ev) \triangleleft tr \text{ for some identifier } k \neq j \\
\stackrel{\rho_k(\text{COR}_k)}{\implies} & \text{ddec}_k.(j, (j, ev)) \triangleleft tr \vee \text{fwd}_k.(j, (j, ev)) \triangleleft tr \vee \text{rdec}_k.(j, (j, ev)) \triangleleft tr \\
\implies & \text{by Lemma 3 (b) and Lemma 3 (c)} \\
& \text{ddec}_k.(j, (j, ev)) \triangleleft tr \\
\stackrel{\rho_k(\text{SRP}_k)}{\implies} & \text{rtreq}_k.(j, ev) \triangleleft tr \\
\stackrel{(*)}{\implies} & \text{rtreq}_k.(j, ev) \triangleleft tr \upharpoonright \alpha(\rho_k(\text{DEL}_k)) \wedge tr \upharpoonright \alpha(\rho_k(\text{DEL}_k)) \in \text{traces}(\rho_k(\text{DEL}_k)) \\
\implies & \text{by definition of } \text{DEL}_k \\
& j = \text{resp}(ev) \\
\implies & \text{with } \text{rtresp}_j.(i, (ev', ev)) \triangleleft tr \text{ from } (\dagger) \text{ above, } ev = ev' \text{ from } (\ddagger) \text{ above,} \\
& \text{and precondition } i = \text{id}(ev) \\
& \text{rtresp}_{\text{resp}(ev)}.(\text{id}(ev), (ev, ev)) \triangleleft tr \quad \square
\end{aligned}$$

Lemma 6. Let trace $tr \in \text{traces}(\text{SI})$, identifier $i \in \text{Id}$, and events $ev, ev' \in \text{CE} \setminus \text{DUM}$ with $ev \otimes ev'$ be given. Then at least one of the following holds:

- $\text{edec}_i.(ev, ev) \not\triangleleft tr$ and for all identifiers $j \in \text{Id}$, $\text{rtresp}_i.(j, (ev, ev)) \not\triangleleft tr$, or
- $\text{edec}_i.(ev', ev') \not\triangleleft tr$ and for all identifiers $j \in \text{Id}$, $\text{rtresp}_i.(j, (ev', ev')) \not\triangleleft tr$.

Proof. Let trace $tr \in \text{traces}(\text{SI})$, identifier $i \in \text{Id}$, and events $ev, ev' \in \text{CE} \setminus \text{DUM}$ with $ev \otimes ev'$ be arbitrary but fixed. Let $D_x := \{\text{edec}_i.(x, x), \text{rtresp}_i.(j, x, x) \mid j \in \text{Id}\}$ for all $x \in \{ev, ev'\}$. We show that $tr \upharpoonright D_{ev} = \langle \rangle$ or $tr \upharpoonright D_{ev'} = \langle \rangle$ holds, which is equivalent to $(tr \upharpoonright D_{ev} \neq \langle \rangle) \implies (tr \upharpoonright D_{ev'} = \langle \rangle)$. Thus, we assume $tr \upharpoonright D_{ev} \neq \langle \rangle$ and show $tr \upharpoonright D_{ev'} = \langle \rangle$.

$$\begin{aligned}
& tr \upharpoonright D_{ev} \neq \langle \rangle \\
\iff & \exists e \in D_{ev}.(e \triangleleft tr) \\
\stackrel{(*)}{\iff} & e \triangleleft tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \wedge tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset))) \\
\iff & \text{with } tr_{\text{dec}} := tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \\
& e \triangleleft tr_{\text{dec}} \wedge tr_{\text{dec}} \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset))) \\
\stackrel{(a)}{\implies} & e \triangleleft tr_{\text{dec}} \wedge tr_{\text{dec}} \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset))) \wedge e \notin D_{ev'} \\
\implies & \text{without loss of generality} \\
& tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) = s_1.\langle e \rangle.s_2 \wedge s_1 \upharpoonright D_{ev'} = \langle \rangle \\
\stackrel{(b)}{\implies} & \exists q \in 2^{\text{CE}}.(ev \in q \wedge \rho_i(\text{DEC}_i(\emptyset)) / (s_1.\langle e \rangle) = \rho_i(\text{DEC}_i(q))) \\
\implies & \text{by Lemma 1 (e)} \\
& s_2 \in \text{traces}(\rho_i(\text{DEC}_i(q))) \wedge ev \in q \\
\stackrel{(c)}{\implies} & s_2 \upharpoonright D_{ev'} = \langle \rangle \\
\implies & \text{with } s_1 \upharpoonright D_{ev'} = \langle \rangle \text{ and } e \notin D_{ev'} \text{ from above} \\
& tr_{\text{dec}} \upharpoonright D_{ev'} = (s_1.\langle e \rangle.s_2) \upharpoonright D_{ev'} = \langle \rangle
\end{aligned}$$

$$\begin{aligned} \implies & \text{by preconditions } D_{ev'} \subseteq \alpha(\rho_i(\text{DEC}_i(\emptyset))) \text{ and } tr_{\text{dec}} = tr \upharpoonright \alpha(\rho_i(\text{DEC}_i(\emptyset))) \\ & tr \upharpoonright D_{ev'} = \langle \rangle \end{aligned}$$

Below, we prove the correctness of the implications labeled (a), (b), and (c) above.

(a) We show that $e \notin D_{ev'}$ follows from $e \triangleleft tr_{\text{dec}} \wedge tr_{\text{dec}} \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset)))$.

$$\begin{aligned} & e \triangleleft tr_{\text{dec}} \wedge tr_{\text{dec}} \in \text{traces}(\rho_i(\text{DEC}_i(\emptyset))) \\ \implies & \text{by definition of } \text{DEC}_i(\emptyset) \text{ and the preconditions } e \in D_{ev} \text{ and } ev \notin DUM \\ & \exists q \in 2^{CE}. (ev \notin \text{conf}(q)) \\ \implies & \text{by definition of } \text{conf} \text{ [1, page 12]} \\ & \neg(ev \otimes ev) \\ \implies & \text{by precondition } ev \otimes ev' \\ & ev \neq ev' \\ \implies & \text{by definition of } D_{ev'} \\ & e \notin D_{ev'} \end{aligned}$$

(b) We show that there exists a state $q \in 2^{CE}$ with $ev \in q$ such that $\rho_i(\text{DEC}_i(\emptyset)) / (s_1 \cdot \langle e \rangle) = \rho_i(\text{DEC}_i(q))$ holds. By definition of $\rho_i(\text{DEC}_i(\emptyset))$, this process only engages in e (recall firstly that e must be one of $\text{edec}_i.(ev, ev)$ and $\text{rtresp}_i.(j, ev, ev)$ for some $j \in Id$, and secondly that $ev \notin DUM$) if it afterwards immediately adds ev to its previously active state, say $q' \in 2^{CE}$, and then behaves as $\rho_i(\text{DEC}_i(q))$ for $q = q' \cup \{ev\}$.

(c) We show by induction over the length of traces s that for all states $q' \in 2^{CE}$ with $ev \in q'$, it holds that $s \in \text{traces}(\rho_i(\text{DEC}_i(q')))$ implies $s \upharpoonright D_{ev'} = \langle \rangle$.

base case ($|s| \leq 1$): By definition of $\text{DEC}_i(q')$, events from $D_{ev'}$ cannot be contained in such a short trace s .

step case ($s = s_1 \cdot s_2$ for $|s_1| = 2$): For all $q' \in 2^{CE}$, the definition of $\text{DEC}_i(q')$ gives:

- An event $e' \in D_{ev'}$ cannot be contained in s_1 because of the definition of conf [1, page 12] and the precondition $ev' \otimes ev$; hence, we have $s_1 \upharpoonright D_{ev'} = \langle \rangle$.
- $\text{DEC}_i(q') / s_1 = \text{DEC}_i(q'')$ for $q'' \supseteq q'$; hence, the induction hypothesis can be applied on q'' and s_2 to obtain $s_2 \upharpoonright D_{ev'} = \langle \rangle$.

It follows that $s \upharpoonright D_{ev'} = \langle \rangle$. □

Based on the preceding lemmas and the auxiliary definition, we can now conduct the proof that the controlled system defined in [1, Section 5] enforces the Chinese Wall policy, which it is instantiated to enforce.

Proof of Theorem 1. We show that $\text{SYSTEM sat } ChW$ holds. Substituting the definitions of sat and ChW , this is equivalent to proving that for all traces $tr \in \text{traces}(\text{SYSTEM})$ there do not exist events $ev_1, ev_2 \in CE$ such that $ev_1, ev_2 \triangleleft tr$ and $ev_1 \otimes ev_2$ hold.

We conduct the proof by contradiction and assume that $\text{SYSTEM sat } ChW$ does not hold. It follows that there exist a trace $tr \in \text{traces}(\text{SYSTEM})$ and events $ev_1, ev_2 \in CE$ such that $ev_1, ev_2 \triangleleft tr$, and $ev_1 \otimes ev_2$ hold true. Since events ev_1 and ev_2 are in conflict, they cannot be dummy events (by definition of dummy events [1, page 11]). Secondly, the responsible node for ev_1 and ev_2 must be the same, i.e., $\text{resp}(ev_1) = \text{resp}(ev_2)$ (by the definition of resp [1, page 12]). Let $k := \text{resp}(ev_1)$ be this responsible node.

In the following, the process SI (see Definition 1 (d)) denotes the instantiated service automata framework with all CSP hiding operations removed and the internal channels renamed to not introduce additional synchronization with the removal of hiding. We assume, without loss of generality, that the service and data providers $PROV_i$ do not make use of local policy-internal channels and the renamed channels either (if they would, we could choose a different renaming). Then by Lemma 2, we obtain $SI \setminus HS = \text{SYSTEM}$, where the hiding set HS is defined in Definition 1 (c). It follows that there must be a trace $tr' \in \text{traces}(SI)$ such that $tr' \upharpoonright \alpha(\text{SYSTEM}) = tr$. Particularly, we therefore have $ev_1, ev_2 \triangleleft tr'$.

Applying Lemma 5 for each event $ev \in \{ev_1, ev_2\}$, we get that in both cases either event $\text{edec}_k.(ev, ev)$ or event $\text{rtrsp}_k.(id(ev), ev, ev)$ is contained in tr' . This contradicts Lemma 6, which states that this can only hold for at most one of ev_1 and ev_2 . Consequently, the assumption that $\text{SYSTEM sat } ChW$ does not hold leads to a contradiction and, thus, cannot be true. Therefore, $\text{SYSTEM sat } ChW$ holds. \square

References

- [1] Richard Gay, Heiko Mantel, and Barbara Sprick. Service Automata. In Gilles Barthe, Anupam Datta, and Sandro Etalle, editors, *Proceedings of the 8th International Workshop on Formal Aspects of Security and Trust*, 2011. In press.
- [2] C. A. R. Hoare. *Communicating Sequential Processes*. Prentice-Hall, Inc., 1985.