6th International Verification Workshop (VERIFY-2010)

What are the verification problems? What are the deduction techniques?

in connection with IJCAR-2010 at FLoC-2010

July 20-21, 2010, Edinburgh, UK

Keynote Speakers

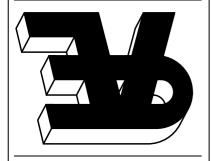
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Abstract submission deadline April 7, 2010

Paper submission deadline

April 11, 2010

Notification of acceptance April 29, 2010

Final version due

May 17, 2010

Workshop e-mail

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CALL FOR PAPERS

The formal verification of critical information systems has a long tradition as one of the main areas of application for automated theorem proving. Nevertheless, the area is of still growing importance as the number of computers affecting everyday life and the complexity of these systems are both increasing. The purpose of the VERIFY workshop series is to discuss problems arising during the formal modeling and verification of information systems and to investigate suitable solutions. Possible perspectives include those of automated theorem proving, tool support, system engineering, and applications.

The VERIFY workshops aim at bringing together people who are interested in the development of safety and security critical systems, in formal methods, in the development of automated theorem proving techniques, and in the development of tool support. Practical experiences gained in realistic verifications are of interest to the automated theorem proving community, and new theorem proving techniques should be transferred into practice. The overall objective of the VERIFY workshops is to identify open problems and to discuss possible solutions under the theme

What are the verification problems? What are the deduction techniques?

The scope of VERIFY includes topics such as

- ATP techniques in verification
- Case studies (specification and verification)
- Combination of verification systems
- Compositional and modular reasoning
- Formal methods for fault tolerance
- Gaps between problems and techniques
- Information-flow security

- Integration of ATPs and CASE-tools
- Management of change
- Refinement and decomposition
- Reliability of mobile computing
- Experience reports on using formal methods Reuse of specifications and proofs
 - Safety-critical systems
 - Security models
 - Tool support for formal methods

Submissions are encouraged in one of the following two categories:

- A. Regular paper: Submissions in this category should describe previously unpublished work (completed or in progress), including descriptions of research, tools, and applications. Papers must be 5-14 pages long (in EasyChair style) or 6-15 pages long (in Springer LNCS style).
- B. Discussion paper: Submissions in this category are intended to initiate discussions and should address controversial issues, and may include provocative statements. Papers must be 3-14 pages long (in EasyChair style) or 3-15 pages long (in Springer LNCS style).

Submission of papers is via EasyChair at

http://www.easychair.org/conferences/?conf=verify2010.

Upon submission, the category (either A or B) should be indicated.

Final versions of accepted papers have to be prepared with LATEX using the EasyChair class available at http://www.easychair.org/easychair.zip. Each accepted paper shall be presented at the workshop and at least one author of each paper must attend the workshop.

In addition to informal proceedings, we envisage a special issue in a journal on the topic of the workshop. Participants of VERIFY-2010 are particularly encouraged to submit a paper to the special issue, but other submissions will also be welcome.

Workshop Web Page: http://www.mais.informatik.tu-darmstadt.de/verify2010/